






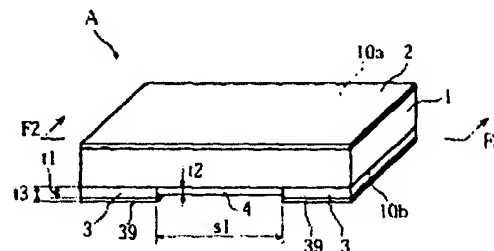


CHIP RESISTOR AND METHOD FOR PRODUCING THE SAME**Publication number:** WO2004010440**Publication date:** 2004-01-29**Inventor:** TSUKADA TORAYUKI (JP)**Applicant:** ROHM CO LTD (JP); TSUKADA TORAYUKI (JP)**Classification:****- International:** H01C1/144; H01C7/00; H01C17/00; H01C17/28;
H05K3/34; H01C1/14; H01C7/00; H01C17/00;
H01C17/28; H05K3/34; (IPC1-7): H01C7/00; H01C17/02**- european:** H01C1/144; H01C7/00D; H01C17/00F; H01C17/28B**Application number:** WO2003JP09292 20030722**Priority number(s):** JP20020215746 20020724; JP20020215747 20020724**Also published as:** US2005266615 (A)
 CN1672222 (A)**Cited documents:** JP2002057009
 JP6215908
 JP7029704
 JP60049611
 US6261927
more >>[Report a data error here](#)**Abstract of WO2004010440**

A chip resistor comprising a resistor (1), an insulating layer (4) formed on the back of the resistor, and two electrodes (3) spaced apart from each other via the insulating layer. Each electrode (3) abuts against the insulating layer (4). A solder layer (39) is formed on the lower surface of each electrode (3).

Data supplied from the **esp@cenet** database - Worldwide